Fig. 1

(Prior Art)

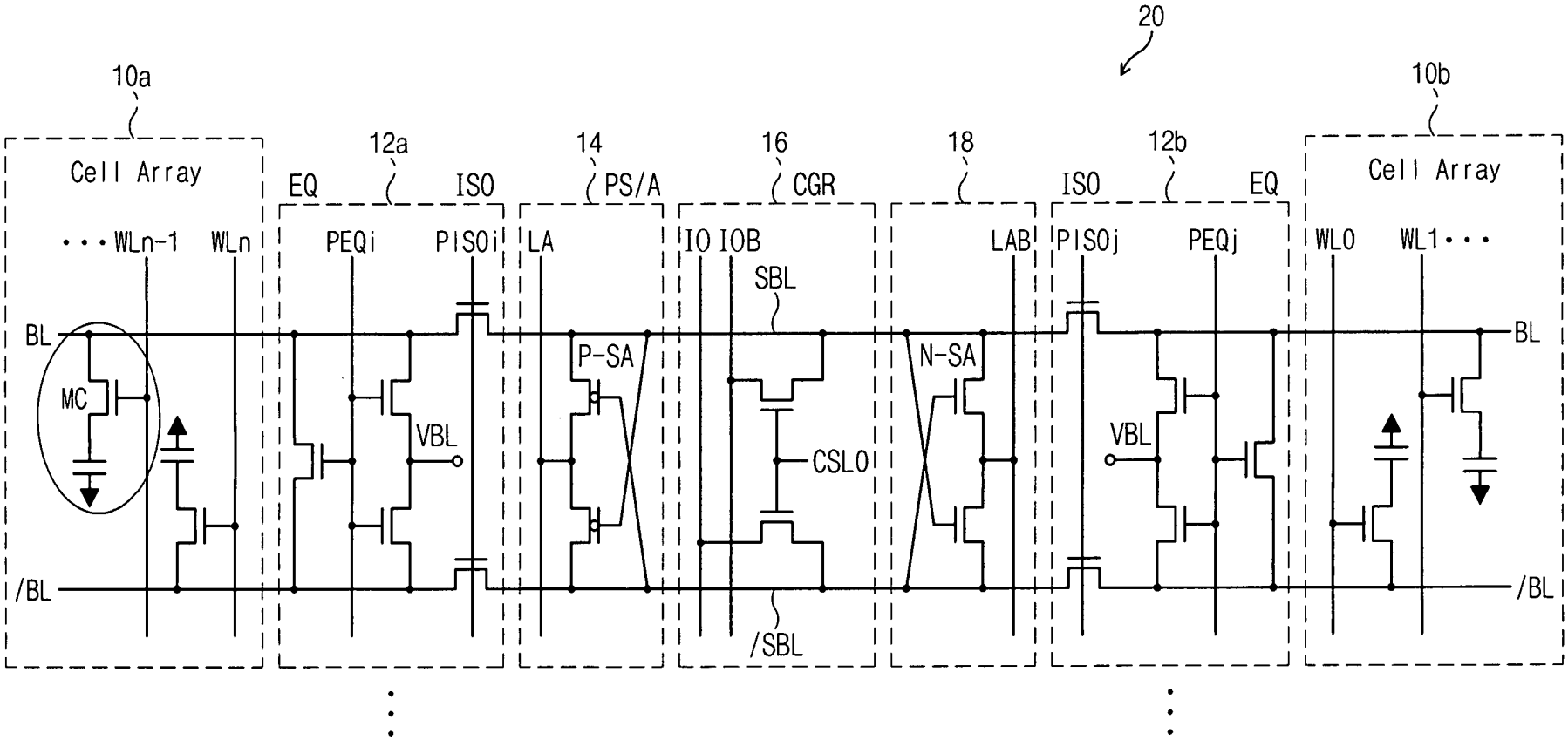


Fig. 2

(Prior Art)

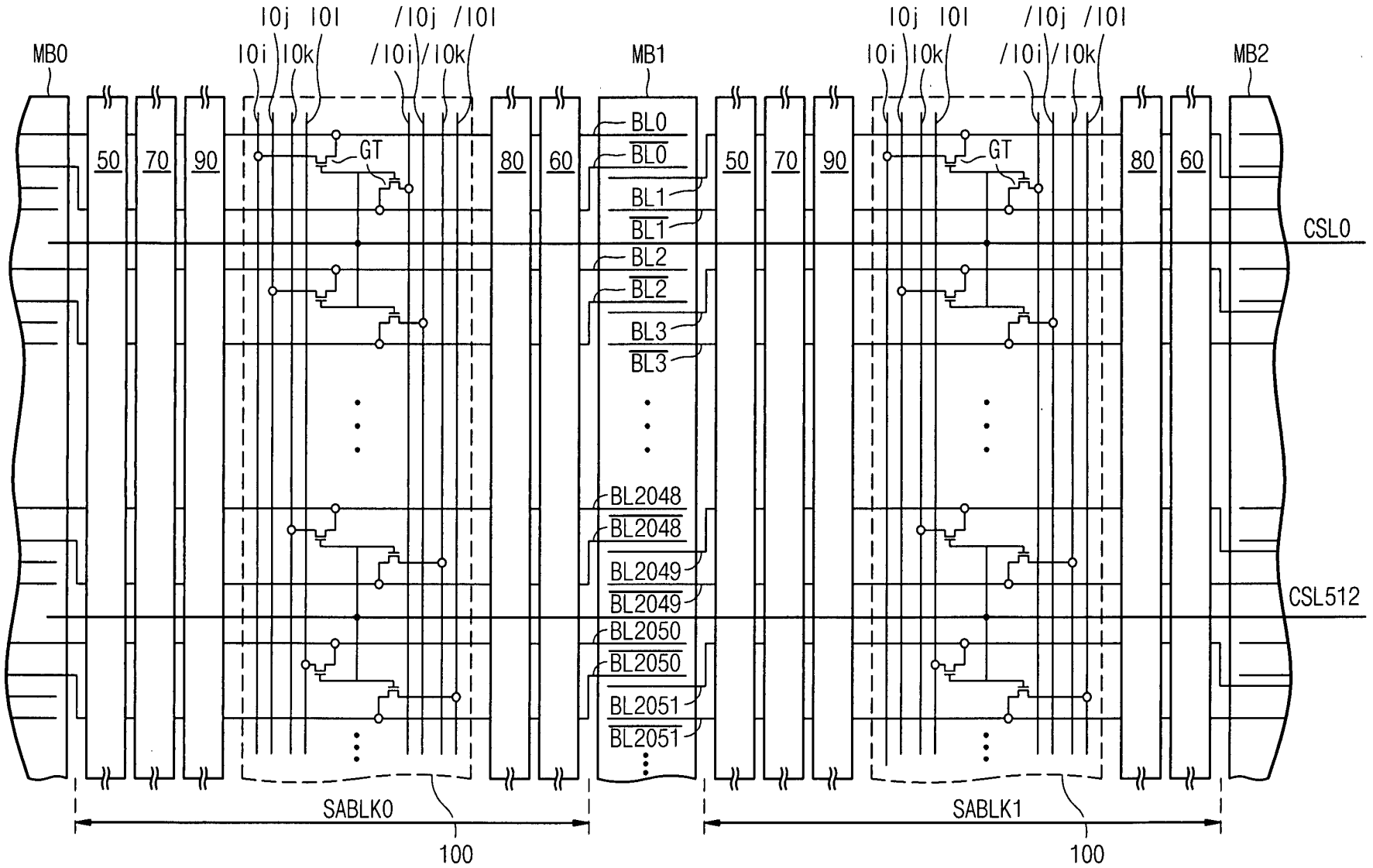


Fig. 3

(Prior Art)

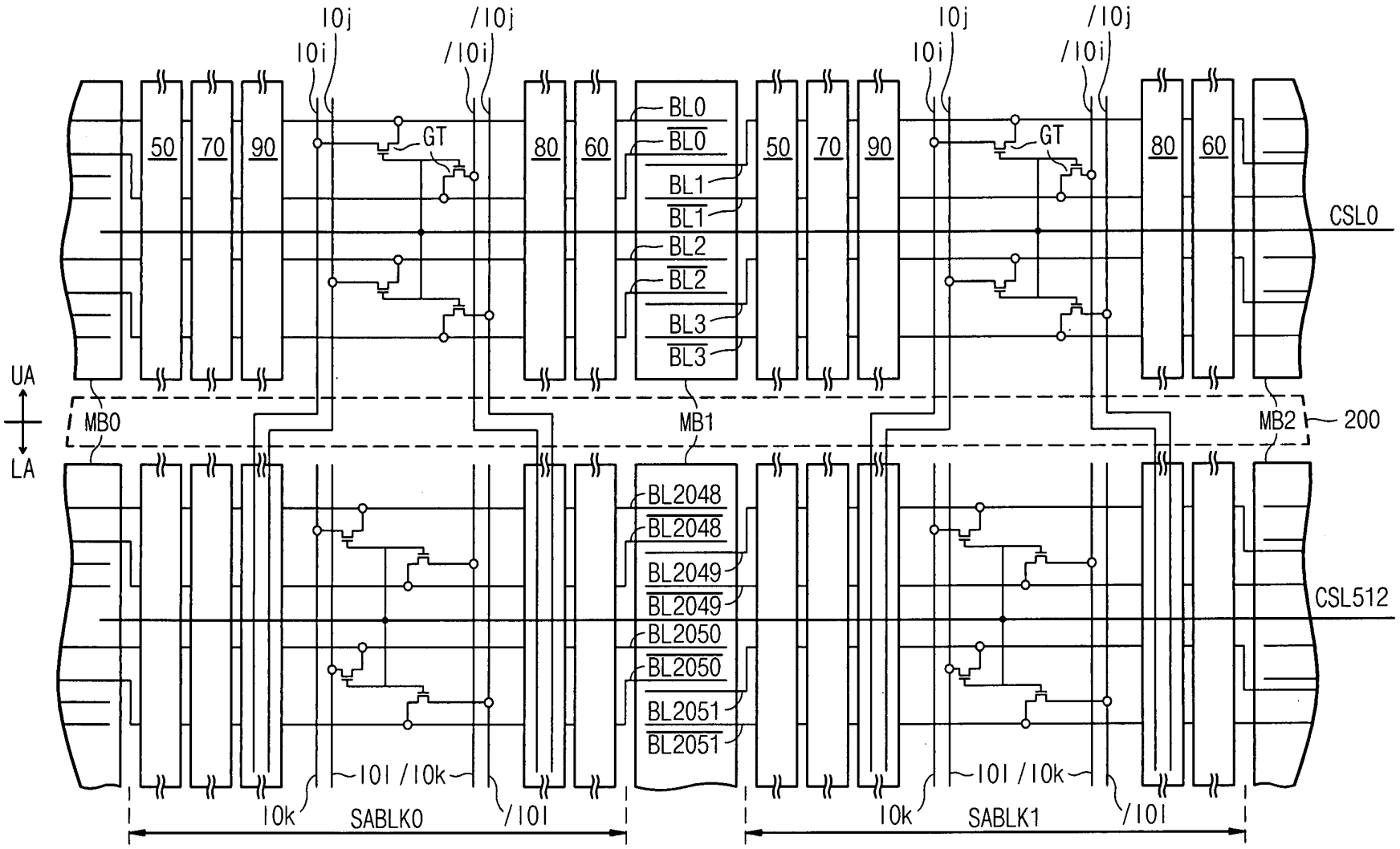


Fig. 4A

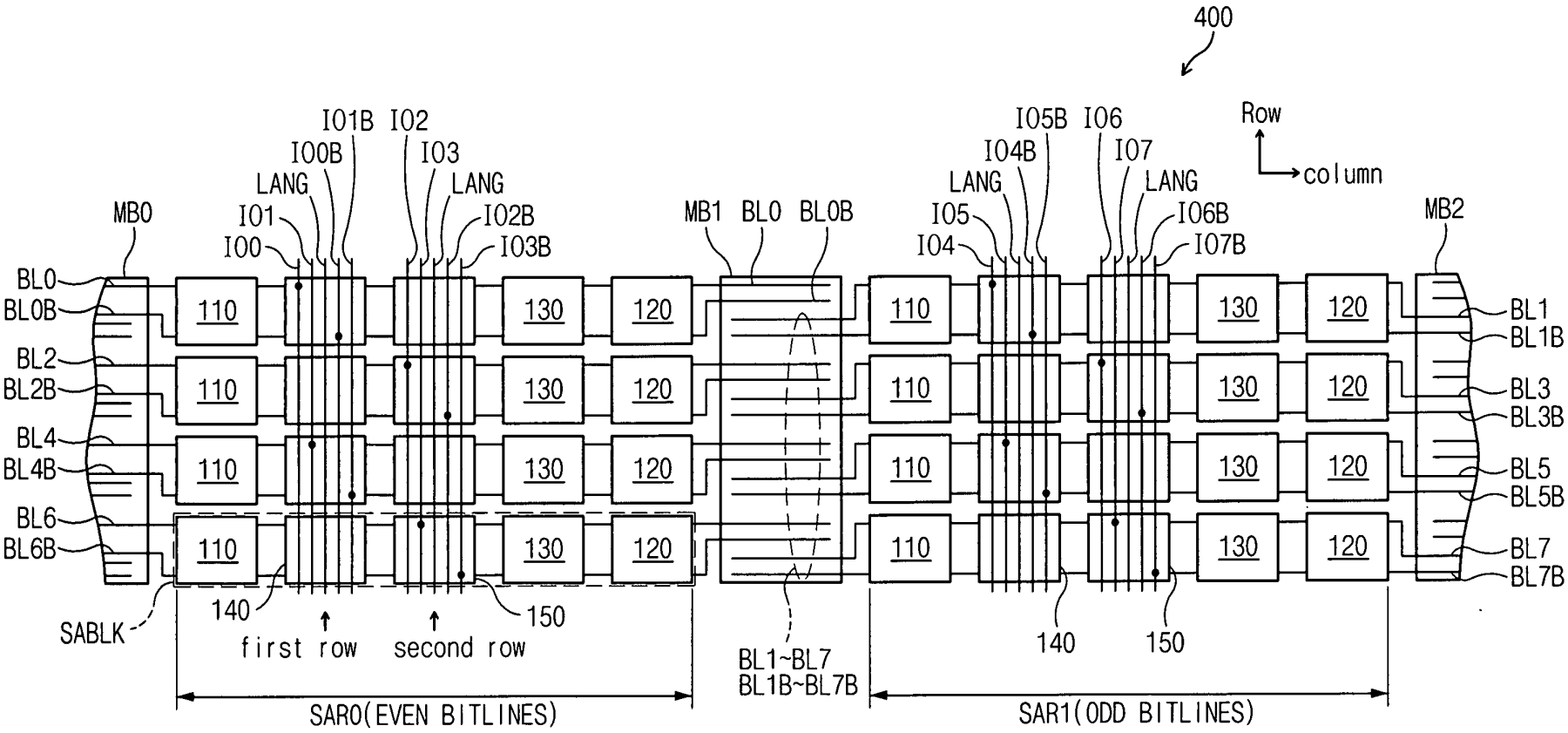

400
Row
column
MB0
MB1
MB2
BL0
BL0B
BL1
BL1B
BL2
BL2B
BL3
BL3B
BL4
BL4B
BL5
BL5B
BL6
BL6B
BL7
BL7B
IO0
IO0B
IO1
IO1B
IO2
IO2B
IO3
IO3B
IO4
IO4B
IO5
IO5B
IO6
IO6B
IO7
IO7B
LANG
110
120
130
140
150
SABLK
first row
second row
BL1~BL7
BL1B~BL7B
SAR0(EVEN BITLINES)
SAR1(ODD BITLINES)

Fig. 4B

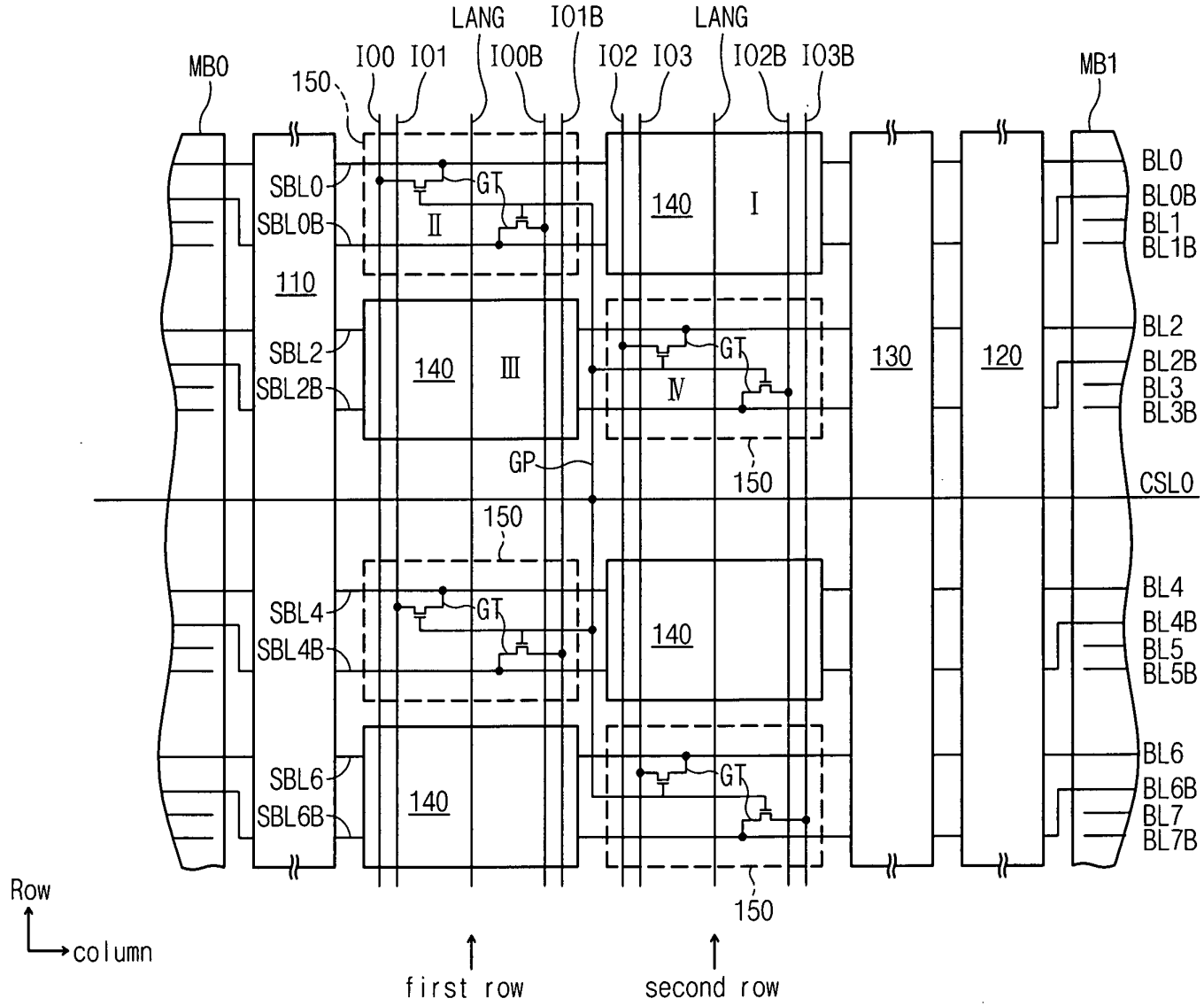

LANG
IO1B
LANG
MB0
150
IO0
IO1
IO0B
IO2
IO3
IO2B
IO3B
MB1
SBL0
SBL0B
GT
II
140
I
BL0
BL0B
BL1
BL1B
110
SBL2
SBL2B
140
III
GT
IV
130
120
BL2
BL2B
BL3
BL3B
GP
150
CSL0
150
SBL4
SBL4B
GT
140
BL4
BL4B
BL5
BL5B
SBL6
SBL6B
140
GT
BL6
BL6B
BL7
BL7B
150
Row
column
first row
second row

Fig. 5A

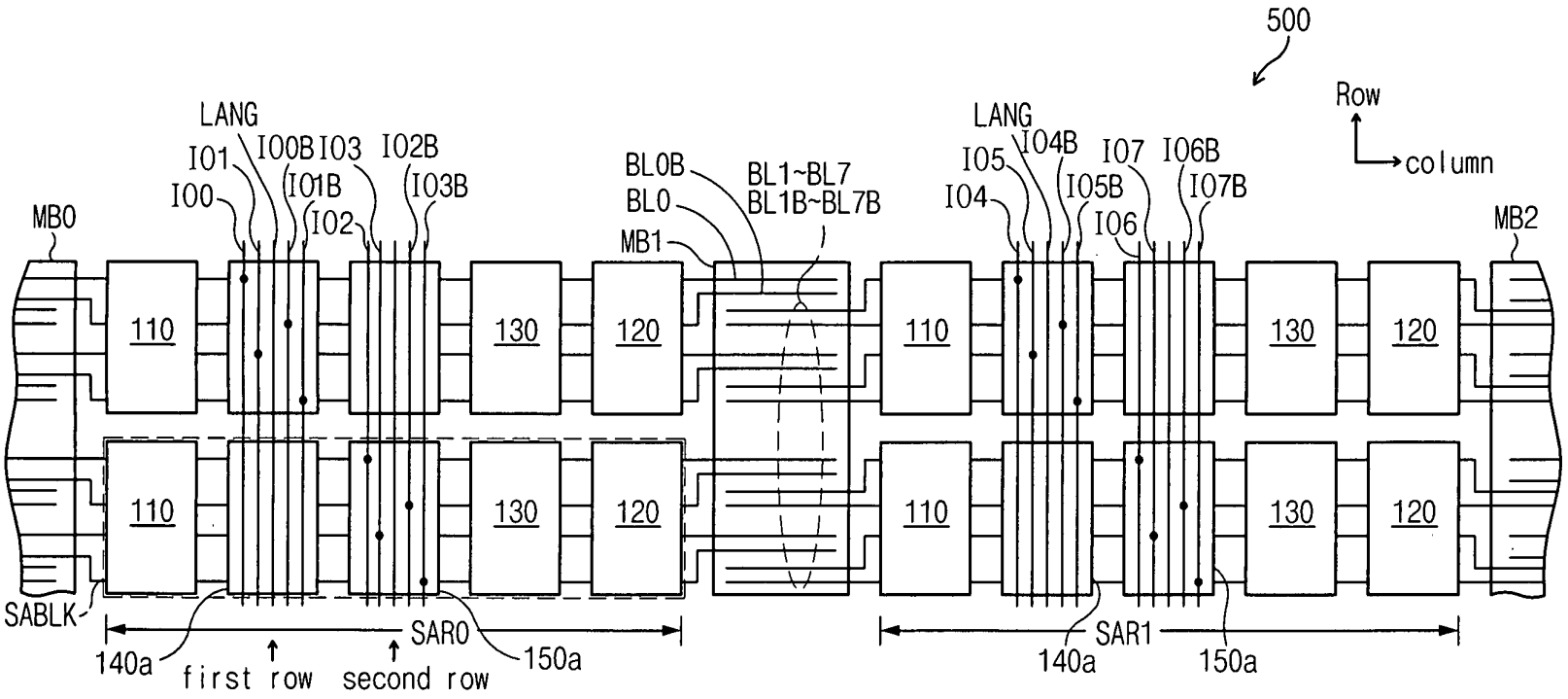

500
Row
column
LANG
IO1
IO0B
IO3
IO2B
IO0
IO1B
IO2
IO3B
MB0
BL0B
BL0
MB1
BL1~BL7
BL1B~BL7B
LANG
IO5
IO4B
IO7
IO6B
IO4
IO5B
IO6
IO7B
MB2
110
130
120
SABLK
SAR0
SAR1
140a
150a
first row
second row

Fig. 5B

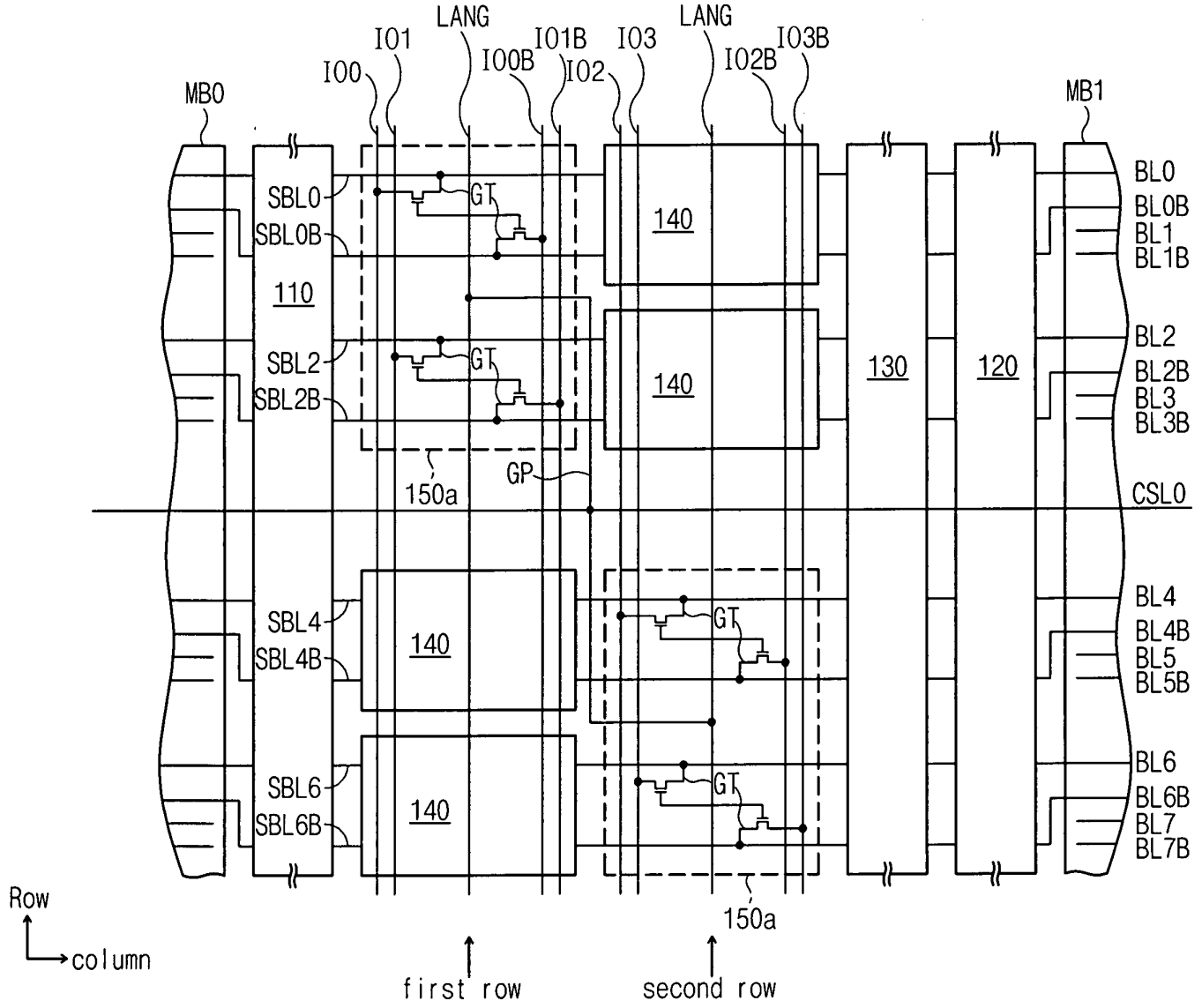

LANG
IO1
IO0
IO0B
IO1B
IO2
IO3
LANG
IO2B
IO3B
MB0
MB1
SBL0
SBL0B
110
SBL2
SBL2B
GT
140
130
120
150a
GP
CSL0
SBL4
SBL4B
SBL6
SBL6B
BL0
BL0B
BL1
BL1B
BL2
BL2B
BL3
BL3B
BL4
BL4B
BL5
BL5B
BL6
BL6B
BL7
BL7B
Row
column
first row
second row

Fig. 6

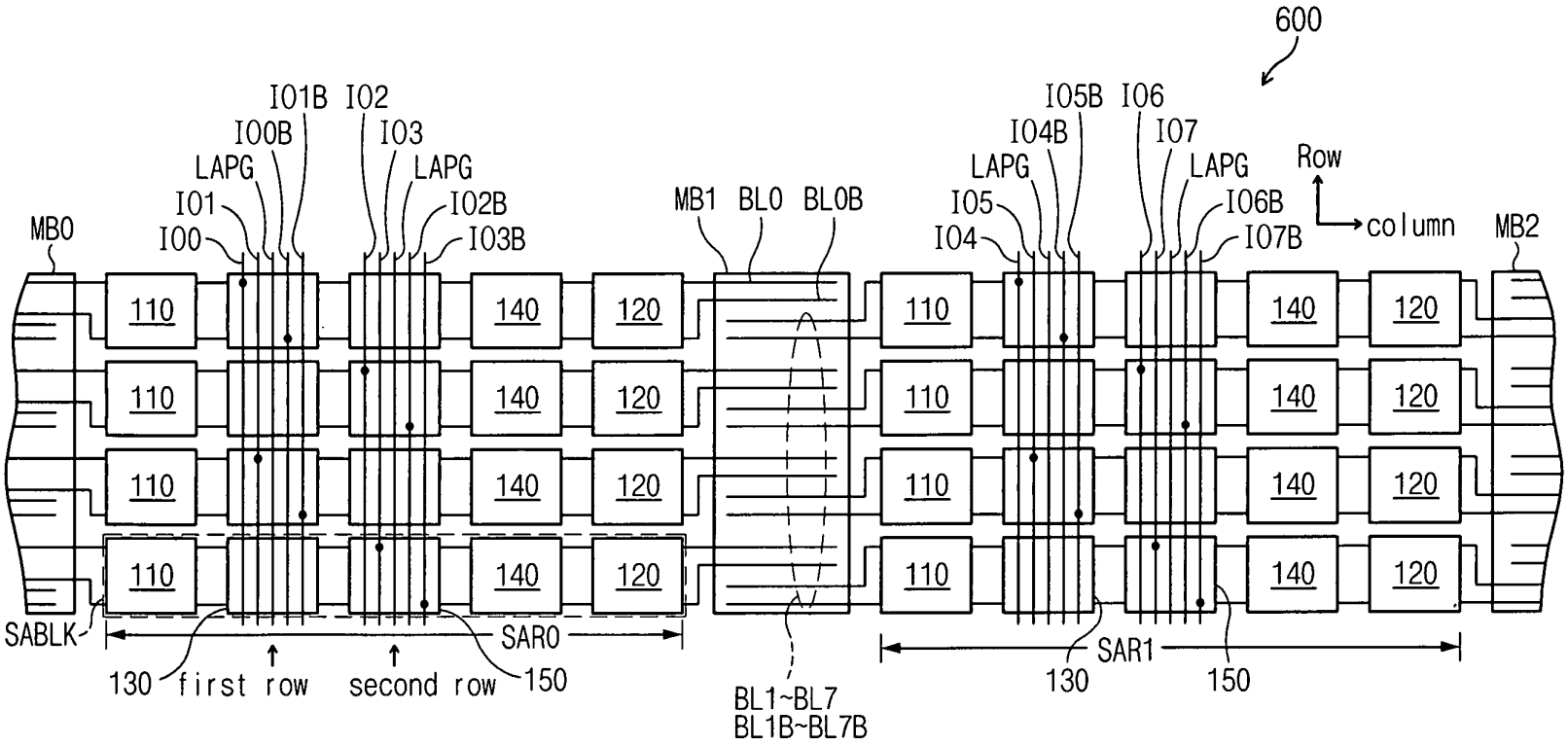

600
IO1B IO2
IO0B IO3
LAPG LAPG
IO1 IO2B
IO0 IO3B
MB0
MB1 BL0 BL0B
IO5B IO6
IO4B IO7
LAPG LAPG
IO5 IO6B
IO4 IO7B
Row
column
MB2
110
140
120
SABLK
SAR0
SAR1
130
first row
second row
150
BL1~BL7
BL1B~BL7B

Fig. 7

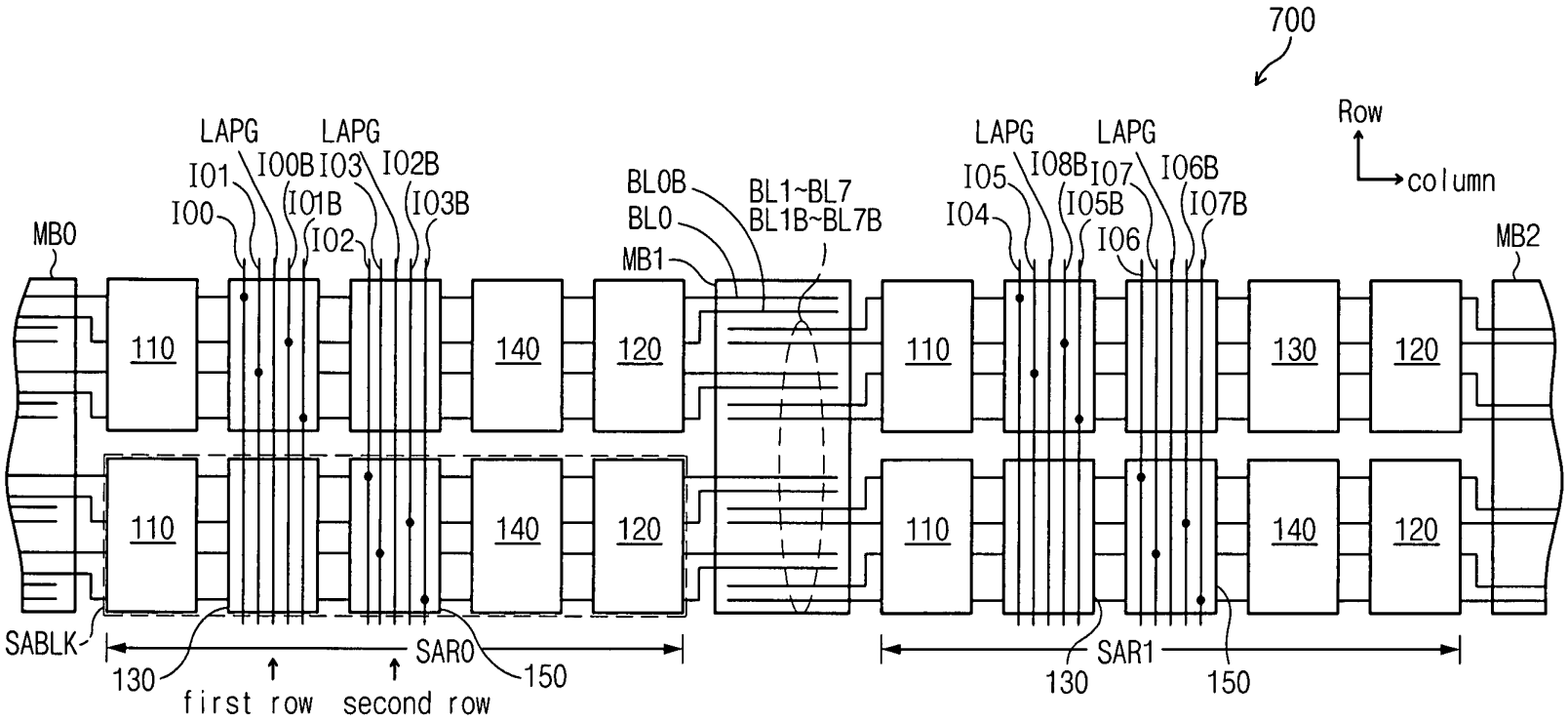
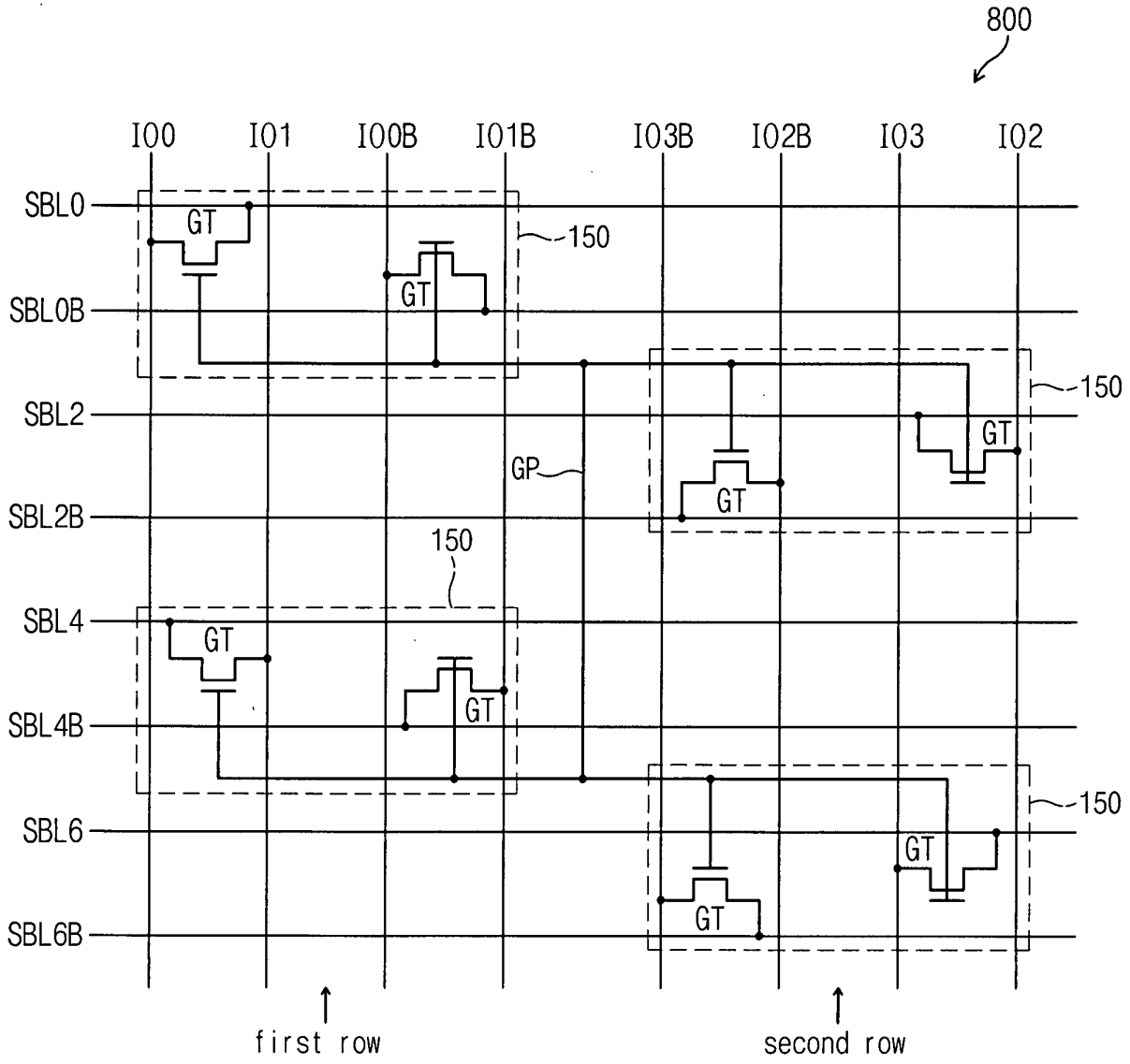

700
Row
column
LAPG
LAPG
LAPG
LAPG
IO1
IO0B
IO3
IO2B
IO5
IO8B
IO7
IO6B
IO0
IO1B
IO2
IO3B
IO4
IO5B
IO6
IO7B
BL0B
BL1~BL7
BL0
BL1B~BL7B
MB1
MB0
MB2
110
140
120
110
130
120
110
140
120
110
140
120
SABLK
SAR0
SAR1
130
150
130
150
first row
second row

Fig. 8


800
IO0
IO1
IO0B
IO1B
IO3B
IO2B
IO3
IO2
SBL0
SBL0B
SBL2
SBL2B
SBL4
SBL4B
SBL6
SBL6B
GT
150
GP
first row
second row